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STAT	EMENT BY	7 Δ	PPLICANT	First Named Inventor	Alina Deutsch	
01711			LIOAN	Group Art Unit		
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Sheet	1	of	1	Attorney Docket Number	YOR920040041US1 (163-30)	

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		RUI WANG, OMAR WING; Transient Analysis of Dispersive VLSI Interconnects Terminated in Nonlinear Loads; IEEE Transactions on Computer-Aided Design, Vol. 11 No. 10 October 1992; pages 1258-1277						
		ALBERT E. RUEHLI AND THOMAS A. JOHNSON; Circuit Analysis Computing of Semiconductor Packages and Systems; Wiley Encyclopedia of Electrical and Electronics Engineering, Vol. 3; pages 339-354						
 								

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